



Case Docket No. HEIWA.026AUS Date: September 26, 2007

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s)

: Noriaki Chiba

Appl. No.

: 10/538,595

Filed

: June 9, 2005

For

: TIMING GENERATION CIRCUIT AND

SEMICONDUCTOR TEST DEVICE HAVING

THE TIMING GENERATION CIRCUIT

Group Art Unit

: 2829

Batch No.

o.

Examiner

: Isla Rodas, Richard

I hereby certify that this correspondence and all marked attachments are being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450 on

September 26, 2007

(Date)

Yasuo Muramatsu, Reg. No. 38.684

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Yasuo Muramatsu Registration No. 38,684 Attorney of Record

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APPLICATION NO. FILING DATE			FIRST NAMED INVE		NTOR		ATTORNEY DOCKET NO.		CONFIRMATION NO.	
10/538,595	06/09/2005	06/09/2005			Noriaki Chiba			HEIWA.026AUS 5434		
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APPLN. TYPE	SMALL ENTITY	ISSU	E FEE DUE	PUBLICATION FEE D	UE	PREV. PAID ISSU	e fee	TOTAL FEE(S) DUE	DATE DUE	
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ISLA RODAS, RICHARD 25			2829	324-158100 02 FC:1504				30	0.00 OP	
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